

Electronic Patent Application Fee Transmittal

Application Number:	10591193
Filing Date:	30-Aug-2006
Title of Invention:	Dft Technique for Stressing Self-Timed Semiconductor Memories to Detect Delay Faults
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Filer:	Mark Alan Wilson
Attorney Docket Number:	US04 0145 US3

Filed as Large Entity

U.S. National Stage under 35 USC 371 Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
Total in USD (\$)				810